

REMARKS

Reconsideration and allowance of the present claims, based on the following remarks, are respectfully requested.

Claims 1, and 25-43 are pending in the present application. Claims 1, 32, and 38 are the independent claim. Claims 2-24 have been cancelled. No new matter has been added.

Claim Rejections under 35 U.S.C. 102(b)

The Office Action rejected Claim 1 under 35 U.S.C. 102(b) as being anticipated by Mori (U.S. Patent 6,287,949). Applicant respectfully disagrees. Claim 1 has been amended to include the following language:

1. A microchip for at least one personal computer configured to operate with at least one other computer connected to at least one network, comprising:

at least one internal firewall, said internal firewall configured to deny access to at least a portion of said microchip from said network;

at least one microprocessor with at least one control unit and at least two processing units, said control unit including means for at least one user of said personal computer to control said at least two processing units;

at least one power management component; and

a plurality of dies, at least two of said dies made by a separate fabrication process and assembled into a package with separate die sections connected directly.

The limitations set forth above are not found in Mori. Thus, Applicant submits that independent Claim 1 is patentable. Independent Claims 32 and 38 have similar language, and

are thus also patentable. Claims 25-31, 33-37, and 39-43 depend, either directly or indirectly, on Claim 1, 32, or 38 and are thus patentable for the same reasons.

The claims are supported, for example, by the following figures and their accompanying explanation:

Claims 1, 32, 38: A microchip for at least one personal computer configured to operate with at least one other computer connected to at least one network, comprising: [FIGS. 1-17, 19-25, and 27H]

at least one internal firewall, said internal firewall configured to deny access to at least a portion of said microchip from said network; [FIGS. 10A-10Q, 16R-16U, 17A-17D, 20A-20B, 21A-21B, 22A-22C, & 25A-25E]

at least one microprocessor with at least one control unit and at least two processing units, said control unit including means for at least one user of said personal computer to control said at least two processing units; [FIGS. 10C-10D, 10L-10M, 10O-10P, 16R-16U, 20B, 21B, & 22A]

at least one power management component; and [FIGS. 10C & 10D]

a plurality of dies, at least two of said dies made by a separate fabrication process and assembled into a package with separate die sections connected directly. [FIGS. 28, 29A-29B, 30A-30C, 31]

Claims 25, 33, 39: FIGS. 10A-10Q, 16R-16U, 17A-17D, 20A-20B, 21A-21B, 22A-22C, 25A-25E

Claims 26, 34, 40: FIGS. 26A-26C, 27A-27F

Claims 27, 35: FIGS. 27A-27H

Claims 28, 36, 41: FIGS. 26A-26C, 27A-27H

Claims 29, 37, 42: FIGS. 27D-27E

Claims 30, 43: FIGS. 23A-23E

Claim 31: FIGS. 22A, 29A

Applicant believes the objections and rejections in the Office Action have been addressed and that the application is now in condition for allowance. The Examiner is invited to contact the undersigned by telephone should the Examiner believe that personal communication will expedite prosecution of this application.

Respectfully submitted,

DLA PIPER US LLP



Dale Lazar
Registration No. 23,872
Attorney of Record

Lisa K. Norton
Registration No. 44,977

PO Box 9271
Reston, Virginia 20195
(703) 773-4000